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ידר	A1	4 0 9 0 2 5 8								5/16/78	Cricchi		365	184		
TP	A2	4	1	4	9	2	7	0	╁	4/10/79	Cricchi et al.		365	222	<u> </u>	
	A3	4	1	8	1	9	8	0	 	1/1/80	McCoy		365	185.03		
	A4	4	2	5	3	0	5	9	\vdash	2/24/81	Bell et al.		365	185.22		
	A5	4	2	7	2	8	3	0	\vdash	6/9/81	Moench		365	45		
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	A7	4	2	8	7	5	7	0	\vdash	9/1/81	Stark		365	104	<u> </u>	
	A8	4	3	9	3	4	7	5	<u> </u>	7/12/83	Kitagawa et a	nl.	365	185.21		
	A9	4	4	1	5	9	9	2	\vdash	11/15/83	Adlhoch		365	207		
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TP	A11	4	4	9	5	6	0	2		1/22/85	Sheppard		365	203		
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TP	B3	ЈР6	6	2	2	5	7	6	9	10/19881	Japan				Abstrac	
TP	B4	JP	5	9	3	1	1	5	8	2/1984	Japan				Abstrac	
TP	B5	DE	3	6	3	7	6	8	2	5/ 1987	Germany				Abstrac	
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TP	C1									EPROM Usin e 1987, pp. 4		te Memory Cells", IEE	E Journal o	f Solid-St	ate	
TP	C2											ord-Erasable EEPRON	M". Alta Fi	eguenza	Vol.	
										o. 487-494.	J		,	-1		
78	C3										Papers of Spr	ing COMPCONS 81, 1	Feb. 23-26,	VLSI		
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77		A12	4	5	8	6	1	6	3		4/29/86	Koike	• • • • • • • • • • • • • • • • • • • •	365	104	
1F		A13	4	6	1	2	6	2	9		9/16/86	Harari		365	185.08	
719		A14	4	7	1	8	0	4	1	L	1/5/88	Baglee et al.	-	365	185.22	
TP		A15	4	7	3	3	3	9	4	<u> </u>	3/22/88	Giebel	<u> </u>	365	201	
TP	1	A16	16 4 7 5 2 9 2 9								6/21/88	Kantz et al.		365	201	
77	•	A17 4 7 6 3 3 0 5 8/9/88 Kuo										Kuo		365	185.22	
TP											10/18/88	Kohda et al.		365	185.09	
719)	A19	9 4 7 9 9 1 9 5								1/17/89	Iwahashi et al		365	185.21	
TP	•	A20	4	8	0	7	1	8	8		2/21/89	Casagrande		365	185.05	
77	•	A21	4	8	0	9	2	3	1		2/28/89	Shannon et al.		365	201	
TF	>	A22	4	8	1	1	2	9	4		3/7/89	Kobayashi et	al.	365	185.22	
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77	P	B6	DE	3	8	3	1	5	3	8	3 1989	Germany				Abstrac
77	D	В7	JP	62	1	8	8	1	0	0	8/1987	Japan				Abstrac
TF	>	B8	JP	0	1	2	3	8	7	8	5/ 1989	Japan				Abstrac
77	Þ	В9	JP	0	1	4	6	9	4	9	10/1989	Japan				Abstrac
	•			0	ТН	ER	DO	CU	ME	NT	S (Including)	Author, Title, L	Date, Pertinent pages,	Etc.)	•	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent pages, Etc.) C4 Furuyama et al, "An Experimental 2-BIT/CELL Storage Dram for Macro Cell or Memory-on-Logic Application", IEEE Custom Integrated Circuits Conference, May 1988, pp. 4.4.1 - 4.4.4																
77	>	C5	Krio 197						NO	S F	ET Memory A	rray", IBM Te	chnical Disclosure Bu	lletin, Vol.	18, No. 12	2, May
77	5	C6	Alb 198								ge Fet Earom	Cell" IBM Tec	chnical Disclosure Bul	letin," Vol.	24, No. 7.	A, Dec
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TP	A23	3 4 8 4 7 8 0 8 7/11/89 Kobatake									Kobatake		365	104		
TP	A24	1 4 8 7 0 6 1 8 9/26/89 Iwasi											365	185.18		
TP	A25	4	9	4	3	9	6	2		7/24/90	Imamiya et al.	•	365	230.08		
779	A26	4	9	5	6	8	1	6		9/11/90	Atsumi et al.		365	185.22		
TP	A27	4	9	9	9	8	1	3		3/12/91	Ohtsuka et al.		365	185,18		
TP	A28												365	189.01		
TP	A29												365	185.02		
מד	A30	5	0	9	5	3	4	4		3/10/92	Harari		257	328		
TP	A31	5	1	3	6	0	2	1		11/10/92	Mehrotra et al	l.	530	350		
TP	A32	5	2	9	7	1	4	8		3/22/94	Harari et al.		365	185.33		
719	A33	5	3	5	7	4	6	2		10/18/94	Tanaka et al.		365	185.22		
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TP	C7		_					-		_		onductor File Memory 1988, No. 1, pp. 27-3	_	LEVELS/(CELL	
TP	C8											e-Polysilicon Technological		Journal of	Solid-	
		Stat	e Ci	rcui	its, v	/ol.\$	SC-2	22, r	10.4	, pp 548-552.	New York, US	S, August 1987				
TP	C9									Synthesizer" 3, pp.196-197		nternational Solid-State	e Circuits (Conference	: :	
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TP	A34	4 5 3 6 1 2 2 7								ij Ŵ1/94	Tanaka et al.		365	185.22		
71P	A35	5	6	0	2	9	8	7		2/11/97	Harari et al.		365	200		
TP	A36	4	9	5	9	8	1	2		9/25/90	Momodomi et	al.	365	185.17		
TP	A37	4	9	3	9	6	9	0		7/3/90	Momodomi et	al.	365	185.17		
TP	A38	4	9	9	6	6	6	9		2/26/91	Endoh et al.		365	185.17		
TP	A39									11/10/1992	Mehrotra et al		365	185.03		
TP	A40							8		12/15/1992	Mehrotra et al. 36.5			185.03		
TP 181	A41	N 2	1	7	2	3	3	8		7/8/1997	Mehrotra et al	. (Reexamination)	365	185.03		
TP	A42	_	6	5	7	2	7	0		3/12/1997	Ohuchi et al.		365	185.22		
TP	A43	5	7	9	3	6	9	6		8/11/1998	Tanaka et al.		365	230.06		
779	A44	5	8	3	1	9	0	3		11/03/1998	Ohuchi et al.		365	185.22		
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TP	C10											MOS Flash Memory", I 88, pp. 1157-1163.	EEE Journ	nal of Soli	d-State	
TP	C11											I Inc., pp. 30-31, 1977.		-		
TP	C12											Group, 45 pp., Nov. 19				
TP	C13											ontrol Receiver and Me no. 3, pp. 103-13, Augu		olay Drive	r"	
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TP	A45	4	6	7	7	5	9	0		6/30/1987	Arakawa	365/185.7	7	00	
TP	A46	4	9	8	0	8	6	1		12/25/1990	Herdt et al.		365	185.17	
TP	A47	4	4	7	5	1	9	4		10/1984	LaVallee et al		365	200	
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	C14	Tor										d Remote Control Rece		ingle Chi	p"
779		IEE	E D	iges	t of	Tec	hnic	cal P	ape	ers, Cat. No. 8	3CH1872-1, 2	pp., June 8, 1983.			
TP	C15		ı Vo									Single Chip Microproc ectronics, vol. CE-28,			
77°	C16	Tor	elli,	"An 7 pp				olog	y F	ully Compatib	ole EAROM Ce	ell":S-ATES Compone	nti Elettro	nici SpA,	no. 6,
TP	C17			et al 982.		LL2	!K-F	LL	Fre	quency Synthe	esizer for 30 Pr	ograms" SGS-ATES,	Technical	Note #152	2, pp.
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TP	C18	Tore 1984		t al.	, "S	ingl	e Cl	nip,	16-	Station TV D	igital Tuning S	ystem" SGS Tec	hnical Note #17	0, pp. 1-1	9,
TP	C19	Scro- Note							ectr	onic Program	Memory Syste	m for TV (32 St	ations)"SGS-AT	ES, Tech	nical
TP		Driv	er" !	SGS	S-A	ΓES	, M	OS	TM	EGRATED (CIRCUITS/M4	PCM Remote Cor 90-M491, 24 pp	. May 1983.		
TP		SGS 1982		TES	s, "P	LL	TV	Mic	croc	omputer Inter	face" SGS-AT	ES, MOS Integra	ated Circuits/M2	06, 15 pp	o., June
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779		Gee et : 1982, p				nced	16K	E2PRC	OM", IE	EEE Journal of	Solid-State Circuits	s, Vol. SC-17	, No. 5, O	ctober
TP C	223	Kynett	et al	., "A	90-1					ogram Cycle 1- pp. 1259-1263	-Mbit Flash Memor	y", IEEE Jou	rnal of So	lid-
TP (C		Tanaka	et a	l., "A	4-1	Abit N	IAN	D-EEP	ROM	with tight Progr	rammed Vt Distribu	ition", 1990	Symposiu	m on
TP C		VLSI Circuits, Digest of Technical Papers, pp. 105-106. 5 IBM Technical Disclosure Bulletin, "EPROMgramming Device", Vol.29, No.9, February 1987, pp. 4145-4146.												
TP C		26 "Bipolar/Mos Memories Data Book," Advanced Micro Devices, ISSN 0888-4714, pp. 6-1-6-14, 1986												
TP C										gy, Inc., pp. 3-1	1-3-7			
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